

Dr. Seshanna Panthala

Awarded St. Thomas Award for Research (3rd Class)

Citation:

Dr. Seshanna Panthala has distinguished himself as the Chairperson of Electrical Engineering of the University and the acute researcher at the University beginning from 1997.

With his expertise in industrial instrumentation, control electronics as well as other related areas of electrical engineering, he does not only lend his expertise and credibility to the courses he teaches and supervises, but also to several research papers he has been putting together and publishing. Some of the research papers conducted by Dr. Seshanna are:

- Thyristor Switched Capacitor Banks for Power Factor Correction
- Sine reference is synchronous with a.c. line
- Design and Development of Power Module for a Six Pulse a.c. to d.c. Controlled Converter
- Behavior of Thyristor Switched Capacitive Load—Part I. pSPICE-Analysis
- Behavior of Thyristor Switched Capacitive Load--Part II. Experimental Analysis
- Development of High Power Station Battery Discharger-Some Design Features
- Energy Saver for Fluorescent Lamps in Group Lighting
- Simple time delay relay reduces inrush current into transformers and capacitors

These and other research efforts bring great recognition to Dr. Seshanna, Faculty of Engineering as well as the University, and thus, he is awarded the St. Thomas Award for Research (3rd Class)